



**Indian Institute of Technology Gandhinagar
Palaj, Gandhinagar
Gujarat – 382355**

07/09/2018

Advertisement

Applications are invited from bright, young candidates for the following posts under the SMDP-C2SD project funded by Ministry of Electronics and IT, Government of India, in Discipline of Electrical Engineering.

Name of Post	: Research Associate
Consolidated Monthly Pay	: Rs. 20,000/- per month consolidated
Qualification and Experience	B. E./B. Tech. or M. E./M. Tech. in Electrical/Electronics/Computer Science Engineering and related disciplines and knowledge in VLSI Design, simulation and testing with preferably a valid GATE score. The job will include design and simulations of VLSI circuits, use of Cadence, Synopsys tools, Testing of ICs, etc. Knowledge of Linux OS, Verilog/VHDL, Python/C/C++ is an added advantage.,
Tenure	: One year extendable up to two years on satisfactory performance

Eligible candidates may apply online (through the following link) on or before **14th October, 2018**.

Shortlisted candidates will be intimated well in advance the date and time of the interview. Selection will be based on interview.

Application Link:

https://docs.google.com/forms/d/1aj07Fs26VItRsviQojDPnDZ5XA78pAI15HLOXgRDAbc/viewform?edit_requested=true