



**Indian Institute of Technology Gandhinagar  
Palaj, Gandhinagar  
Gujarat – 382355**

## **Advertisement**

Applications are invited from bright, young candidates for the following posts under the SMDP-C2SD project funded by DeITY, Government of India, in Discipline of Electrical Engineering.

Name of Post : Lab Engineer  
Consolidated Monthly Pay : Rs. 34,000/- per month consolidated  
Qualification and Experience : M. E./M. Tech. in Electrical/Electronics/Computer Science Engineering and related disciplines with a valid GATE score and knowledge in VLSI Design (Analog or Digital) : or Microelectronics or equivalent. Working knowledge of VLSI CAD tools like Cadence/Synopsis/Mentor Graphics is required. Experience in LINUX operating system is desired.  
Tenure : One year extendable up to five years on satisfactory performance

Name of Post : Project Associate  
Consolidated Monthly Pay : Rs. 20,000/- per month consolidated  
Qualification and Experience : B. E./B. Tech. or M. E./M. Tech. in Electrical/Electronics/Computer Science Engineering and related disciplines with a valid GATE score and knowledge in VLSI Design (Analog or Digital) or Microelectronics or equivalent. Working knowledge of VLSI CAD tools like Cadence/Synopsis/Mentor is preferred. Experience in LINUX operating system is desired.  
Tenure : One year extendable up to five years on satisfactory performance

Eligible candidates may send their bio-data to **Dr. Nihar Ranjan Mohapatra, Department of Electrical Engineering, IIT Gandhinagar, by e-mail (nihar@iitgn.ac.in)** on or before 30th May 2016. Selection will be based upon interview and shortlisted candidates will be intimated well in advance the date and time of the interview. The interview will be conducted in the 4<sup>th</sup> week of June.