



IIT Gandhinagar invites applications from BE/BTech/MSc with a valid GATE/NET score or ME/MTech (Electrical/Electronics Engineering/Physics/Material Sciences) students for a **special round of PhD admissions in the discipline of Electrical Engineering** with a focussed area of Modeling and Simulation of Semiconductor Devices.

The topics of interest include (but are not limited to):

- Design of emerging 2D materials-based devices.
- Modeling of brain-inspired memory devices.
- Multi-scale simulation of atomic-scale devices.
- Monolithic 3D integrated circuits.

Applications will continue to be screened until the position is filled.

Queries may be directed to **Dr. Tarun Kumar Agarwal** via email (tagarwal@iitgn.ac.in).

**APPLICATION
DEADLINE**

July 3, 2022

**APPLICATION
FORM**

tiny.cc/PhD-EE

